



深圳市拓普微科技开发有限公司

SHENZHEN TOPWAY TECHNOLOGY CO., LTD.

LMT035ENAFWA-NND

LCD Module User Manual

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0.1	- Preliminary Draft release	2020-08-25

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1 .General Specifications

Feature		Spec
Display Spec.	Size	3.5 inch
	Resolution	320(RGB)x240
	Interface	RGB 24 bit
	Color Depth	16.7M
	Technology Type	a-Si
	Pixel pitch(mm)	0.219x0.219
	Pixel Configuration	R.G.B Vertical Stripe
	Surface Treatment	Glare
	Viewing Direction	Full View
Mechanical Characteristics	LCM (W x H x D) (mm)	76.9x64.2x5.3
	Active Area(mm)	70.08x52.56
	Driver IC	ST7272-G4
	With /Without TSP	Without TSP
	LED Numbers	6LEDs

Note 1:Requirements on Environmental Protection: RoHS

Note 2: The main FPC and plastic frame can fulfill UL94-V0

2 .Input/Output Terminals

2.1 TFT LCD Panel

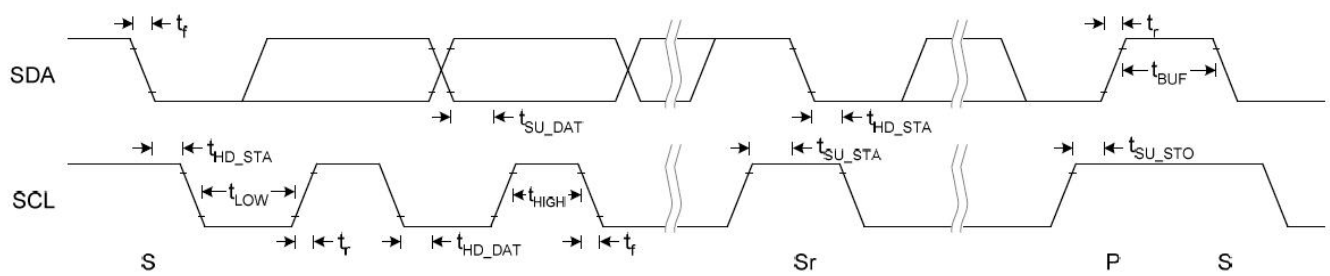
No	Symbol	I/O	Description	Comment
1~2	VBL-	P	CATHODE OF BACKLIGHT LED	
3~4	VBL+	P	ANODE OF BACKLIGHT LED	
5	NC	-	NC	
6	/RESET	I	SYSTEM Reset pin.internal pull high	
7	CSB	I	Chip select pin of serial interface.internal pull high	
8	SCL	I	Clock pin of serial interface。 Internal pull high	
9~10	SDA	I/O	Serial communication data input and output.	
11~18	B7~B0	I/O	BLUE DATA	
19~26	G7~G0	I/O	GREEN DATA	
27~34	R7~R0	I/O	RED DATA	
35	DCLK	I	Dot-clock signal and oscillator source.	
36	HSYNC	I	Line synchronization siganl	
37	VSYNC	I	Frame synchronization siganl	
38	ENB	I	Diaplay enable pin from controller	
39~40	VDD(ANALOG)	P	Power supply for analog circuit. Power supply for digital I/O pins.	
41~42	NC	-	NC	
43	NC(YU)	-	NC(Touch panel Y up line)	
44	NC(XR)	-	NC(Touch panel X right line)	
45	NC(XD)	-	NC(Touch panel Y down line)	
46	NC(XL)	-	NC(Touch panel X left line)	
47	SHUT	I	Dicplay shut down pin to put the drive into sleep mode. A sharp falling edge must be provided to such pin when IC power on. Internal pull low -Connect to VDDIO for sleep mode. -Connect to VSS for normal operating mode.	
48	NC	-	NC	
49~50	VSS	P	System ground pin of the IC	

Note 1: I——Input, O——Output, P——Power/Ground,

2.2 K2 Interface

Pin No.	Pin Name	I/O	Descriptions
1	VDD	P	Positive Power Supply
2	GND	P	Power Ground (0V)
3	I2C RST	I	Interrupt Signal
4	TP_SCL	I	Serial Input Clock
5	TP_SDA	I	Serial Input Data
6	Global RST	I	Reset Signal

2.3 K2 I2C Interface Descriptions



I2C Timing Diagram

Conditions: VDD = 3.3V, GND = 0V, T_A = 25°C

Symbol	Parameter	Rating			Unit
		Min.	Typ.	Max.	
f _{SCL}	SCL clock frequency	0	-	400	kHz
t _{LOW}	Low period of the SCL clock	1.3	-	-	us
t _{HIGH}	High period of the SCL clock	0.6	-	-	us
t _f	Signal falling time	-	-	300	ns
t _r	Signal rising time	-	-	300	ns
t _{SU_STA}	Set up time for a repeated START condition	0.6	-	-	us
t _{HD_STA}	Hold time (repeated) START condition. After this period, the first clock pulse is generated	0.6	-	-	us
t _{SU_DAT}	Data set up time	100	-	-	ns
t _{HD_DAT}	Data hold time	0	-	0.9	us
t _{SU_STO}	Set up time for STOP condition	0.6	-	-	us
t _{BUF}	Bus free time between a STOP and START condition	1.3	-	-	us
C _b	Capacitive load for each bus line	-	-	400	pF

Explain:

This touch screen using chip ST1633i, About, absolute maximum ratings, electrical characteristics, clock and data input timing diagram, chip ST1633i command descriptions, Please reference chip ST1633i material document.

3 .Absolute Maximum Ratings

3.1 Driving TFT LCD Panel

Ta =25℃

Item	Symbol	MIN	MAX	Unit	Remark
Power Voltage	VDD	--	4.0	V	
Back Light Forward Current	I _{LED}	--	25	mA	For each LED
Back Light Forward Current Voltage	V _{LED}	--	18.6	V	
Operating Temperature	Top	-20	70	℃	
Storage Temperature	Tst	-30	80	℃	
Relative Humidity Note1	RH	--	90	%	Note2
Absolute Humidity	AH	--	≤70	g/m ³	Ta>70℃

Note1: Ta means the ambient temperature.

It is necessary to limit the relative humidity to the specified temperature range.

Condensation .on the module is not allowed.

Note2: Ta≤60 ℃: 90% RH MAX (96HRS MAX)

Ta≤60 ℃: ABSOLUTE HUMIDITY MUST BE LOWER THAN THE HUMIDITY OF 90%RH AT 60℃ (96HRS MAX)

4 .Electrical Characteristics(CTP)

4.1 Driving TFT LCD Panel

GND=0V, T_{OP} =25℃

Items	Symb ol	MIN.	TYP .	MAX.	Unit	Applicable Pin
Operating Voltage	V _{DD}	2.8	3.3	3.6	V	VCC
Input High Voltage	V _{IH}	0.85*VDD	-	-	V	Input pins
Input Low Voltage	V _{IL}	-	-	0.15*VDD	V	Input pins
Normal Current	I _{CC}	-	16.1	24	mA	VDD
Idle Current	I _{IDLE}	-	8.1	12.2	mA	VDD
Power Down Current	V _{IPD}	-	-	20	uA	VDD

5 .Electrical Characteristics(FTF)

5.1 Driving TFT LCD Panel

GND=0V, Ta=25℃

Item	Symbol	MIN	TYP	MAX	Unit	Remark
Supply Voltage	VDD	3.0	3.3	3.6	V	
Logic Signal Voltage	VDD	3.0	3.3	3.6	V	
Input Signal Voltage	Low Level	V _{IL}	--	0.3* VDD	V	
	High Level	V _{IH}	0.7* VCC	--	V	
Output Signal Voltage	Low Level (VOL)	I _{OL} =+1.0mA	DGND	DGND+0.4	V	
	High Level (VOH)	I _{OH} =-1.0mA	VDDI-0.4	VDDI	V	
operating current	Analog operating current	I _{DD}	-	20	mA	Displon on Static VDD=3.3V Without Backlight
	Digital operating current	I _{CC}	-	1	mA	Displon on Static Vcc=3.3V Without Backlight

5.2 Backlight Unit

Ta=25℃

Item	Symbol	MIN	TYP	MAX	Unit	Remark
Forward Current	IF	—	20	25	mA	Note 4
Forward Current Voltage	VF	—	18	18.6	V	Note 1,2
Backlight Power Consumption	WBL	—	360	465	mW	For total LEDs
LED lifetime	L	30000	40000	—	Hours	Note1,2,3,4



Note1: The LED driving condition is defined for each LED module (1 LED Serial, 1 LED Parallel).

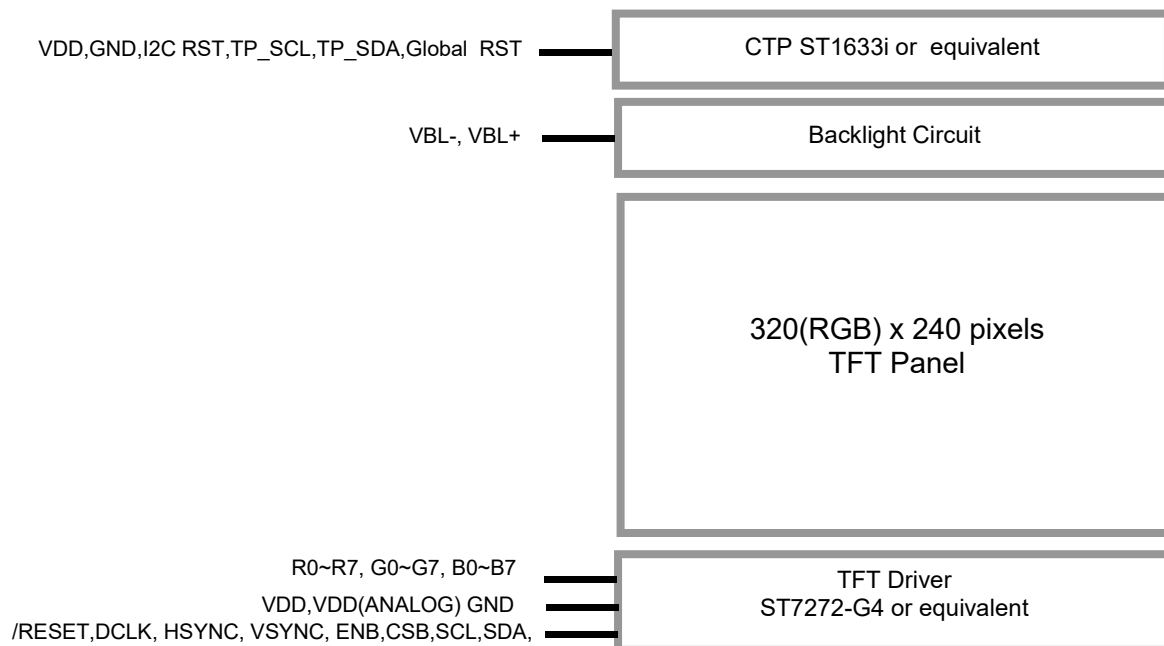
Note2: Under LCM operating, the stable forward current should be inputted. And forward voltage is for reference only.

Note3: Optical performance should be evaluated at Ta=25℃ only. If LED is driven by high current, high ambient temperature & humidity condition. The life time of LED will be reduced. Operating life means brightness goes down to 50% initial brightness. Typical operating life time is estimated data. At the same time the luminance of Backlight would decrease under the high temperature.

Note4: The LED driving condition is defined for each LED module.

5.3 Block Diagram

LCD module diagram

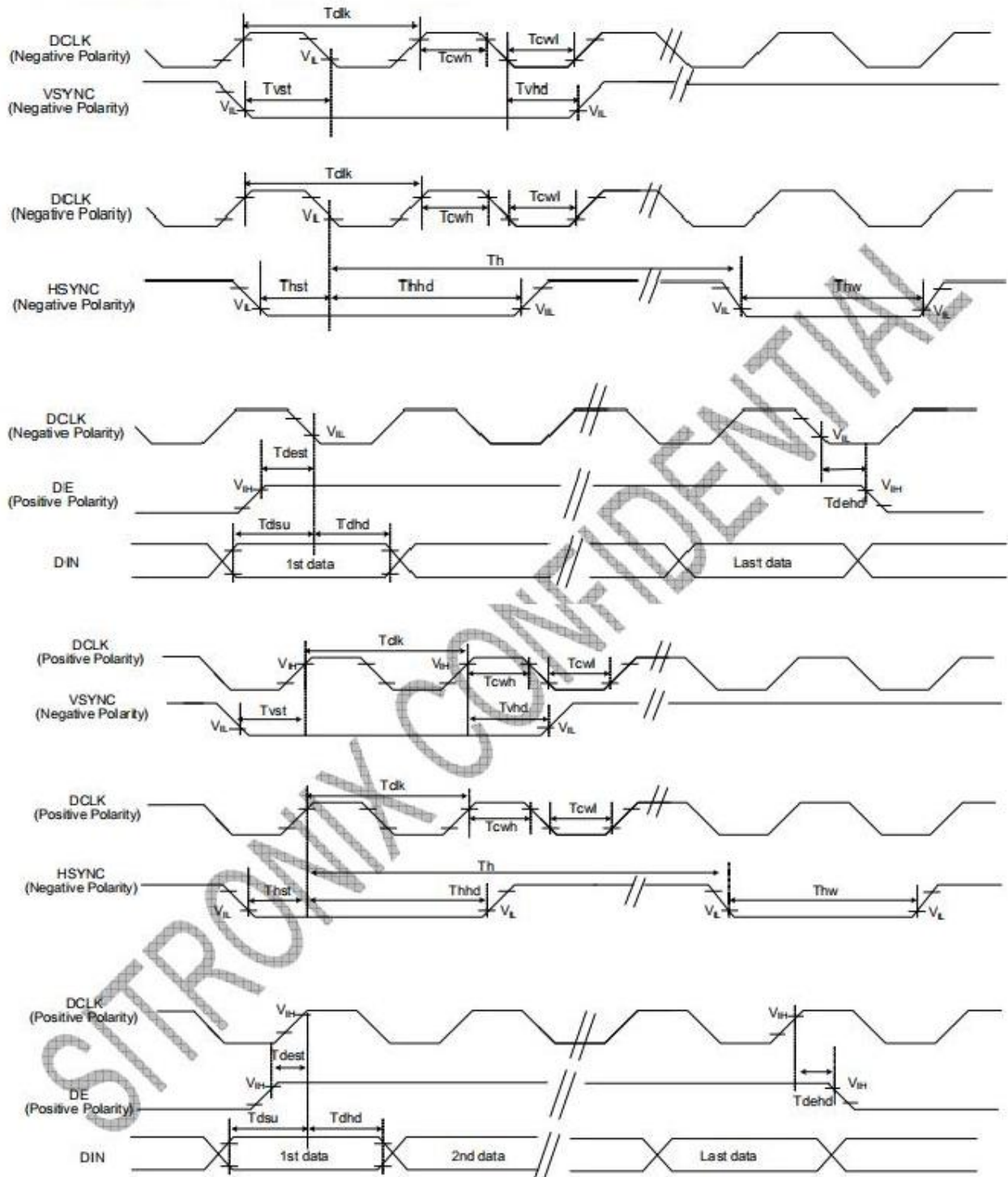


6 .Timing Chart

INTERFACE TIMING

Note: Please refer to ST7272A datasheet for more details.

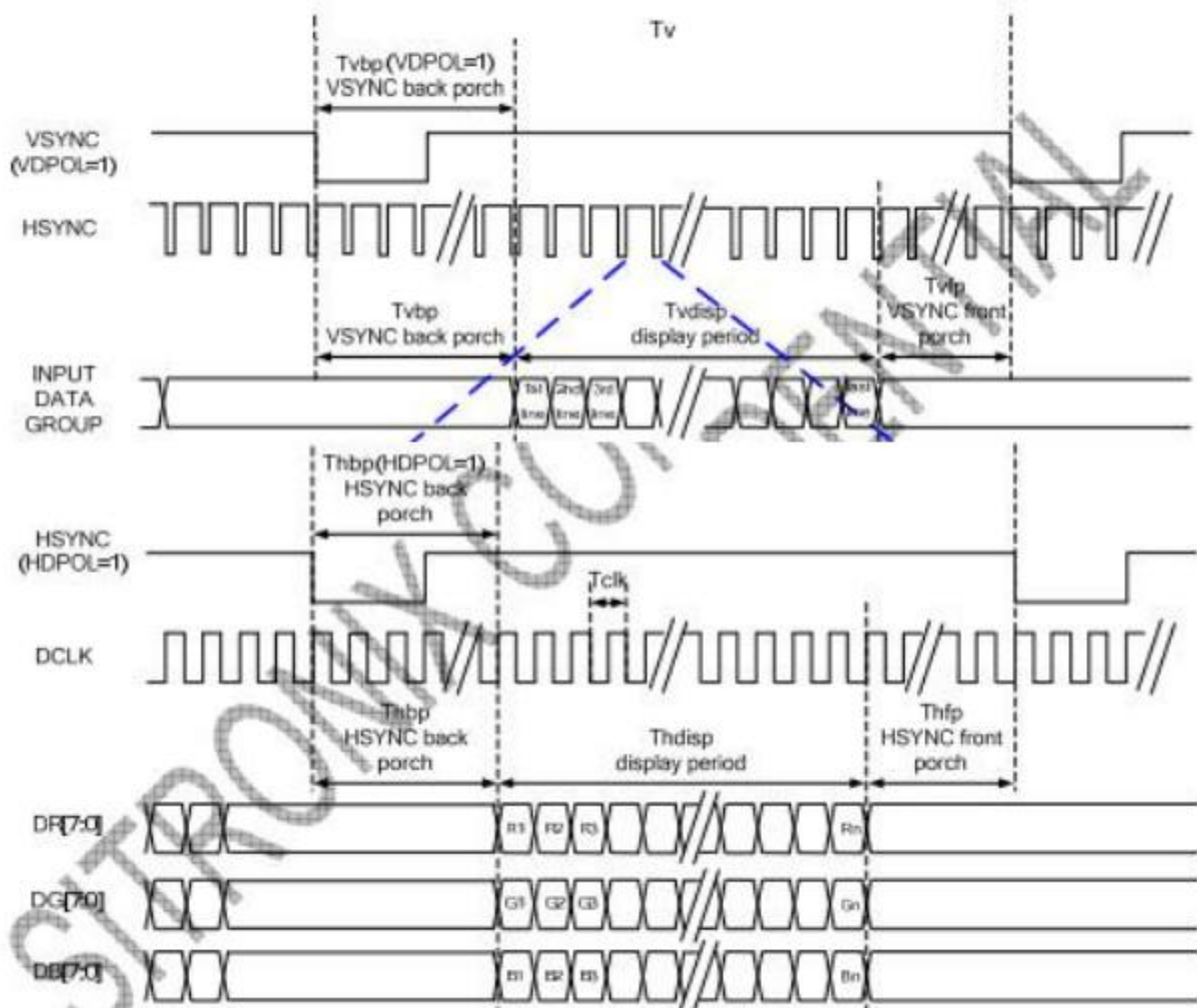
6.1 System Bus Timing for RGB Interface



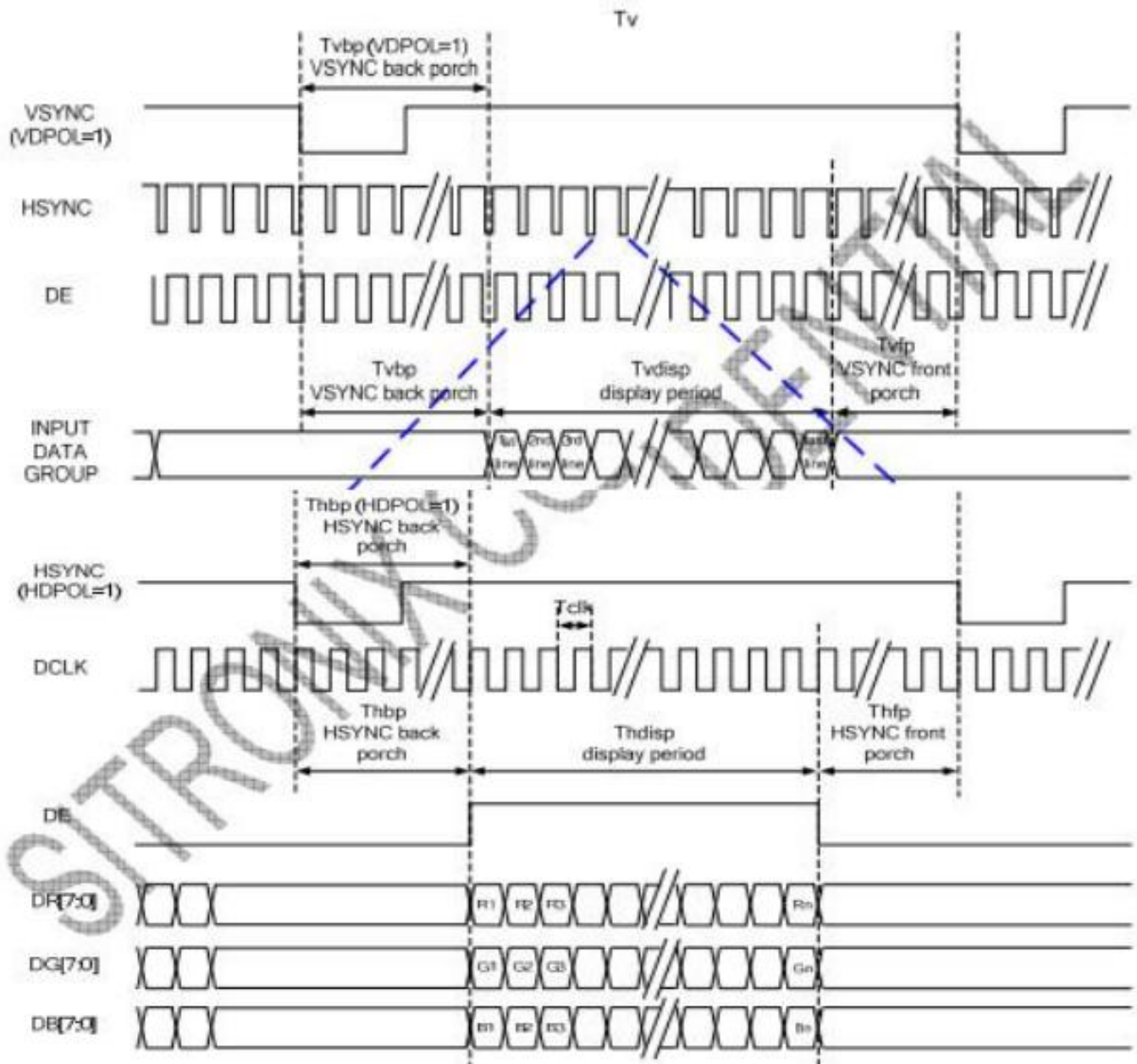
Item	Symbol	Min.	Typ.	Max.	Unit	Conditions
CLK Pulse Duty	Tclk	40	50	60	%	
HSYNC Width	Thw	2	-	-	DCLK	
HSYNC Period	Th	55	60	65	us	
VSYNC Setup Time	Tvst	12	-	-	ns	
VSYNC Hold Time	Tvhd	12	-	-	ns	
HSYNC Setup Time	Thst	12	-	-	ns	
HSYNC Hold Time	Thhd	12	-	-	ns	
Data Setup Time	Tdsu	12	-	-	ns	
Data Hold Time	Tdhd	12	-	-	ns	
DE Setup Time	Tdest	12	-	-	ns	
DE Hold Time	Tdehd	12	-	-	ns	

6.2 RGB Interface

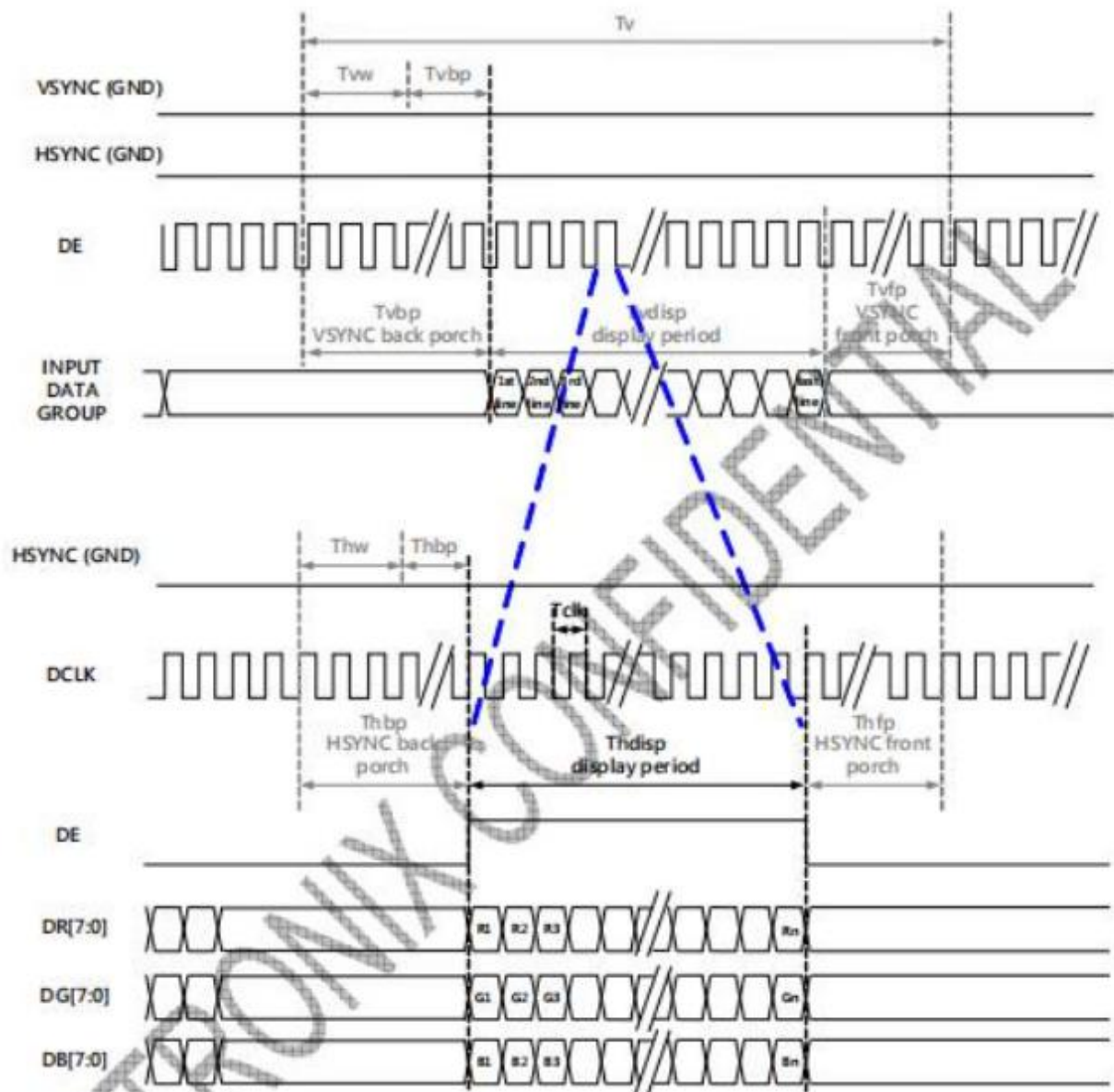
SYNC Mode



6.3 SYNC-DE Mode



6.4 DE Mode



RGB Mode Selection Table	DCLK	HSYNC	VSYNC	DE
SYNC - DE Mode	Input	Input	Input	Input
SYNC Mode	Input	Input	Input	GND
DE Mode	Input	GND	GND	Input

6.5 Parallel 24-bit RGB Input Timing Table

(GND=0V, TA=25°C)

Parallel 24-bit RGB Input Timing Table							
Item		Symbol	Min.	Typ.	Max.	Unit	Note
DCLK Frequency		Fclk	5	6	8	MHz	
DCLK Period		Tclk	125	167	200	ns	
HSYNC	Period Time	Th	325	371	438	DCLK	
	Display Period	Thdisp		320		DCLK	
	Back Porch	Thbp	3	43	43	DCLK	SYNC mode back porch control by H_BLANKING[7:0] setting Thbp= H_BLANKING[7:0]
	Front Porch	Thfp	2	8	75	DCLK	
	Pulse Width	Thw	2	4	43	DCLK	
VSYNC	Period Time	Tv	244	260	289	HSYNC	
	Display Period	Tvdisp		240		HSYNC	
	Back Porch	Tvbp	2	12	12	HSYNC	SYNC mode back porch control by V_BLANKING[7:0] setting Tvbp= V_BLANKING[7:0]
	Front Porch	Tvfp	2	8	37	HSYNC	
	Pulse Width	Tvw	2	4	12	HSYNC	

7 .Optical Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit	Remark
View Angles	θT	CR≥10	70	80	-	Degree	Note2,3,8
	θB		70	80	-		
	θL		70	80	-		
	θR		70	80	-		
Contrast Ratio	CR	θ=0°	640	800	-		Note 3
Response Time	T _r +T _F	θ=0°	-	30	40	msec	Note 4
Chromaticity	White	x	0.250	0.300	0.350		Note 1,5
		y	0.260	0.310	0.360		
	Red	x	0.574	0.624	0.674		Note 1,5
		y	0.308	0.358	0.408		
	Green	x	0.322	0.372	0.422		Note 1,5
		y	0.540	0.590	0.640		
	Blue	x	0.098	0.148	0.198		Note 1,5
		y	0.033	0.083	0.133		
Uniformity	U	Backlight is on	80	-	-	%	Note 6
Flicker	-		-	-	25	%	CA310
NTSC	-		-	60	-	%	Note 5
Luminance	L		360	400	-	cd/m ²	Note 7

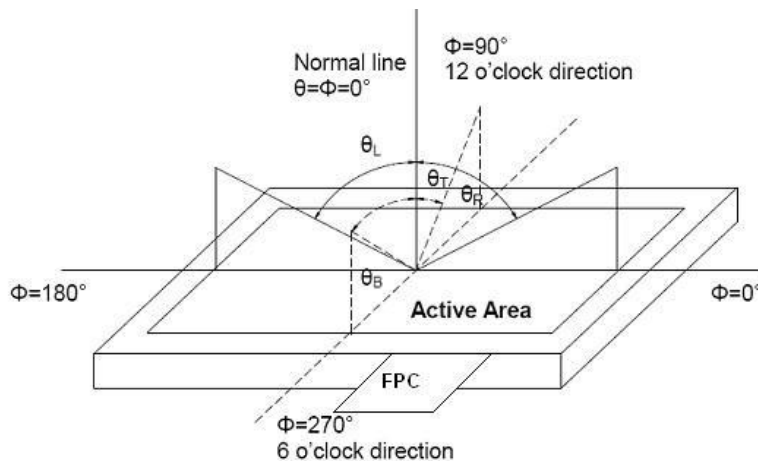
Test Conditions:

1. $I_F = 20\text{mA}$, and the ambient temperature is 25°C .
2. The test systems refer to Note 1 and Note 2.
3. Note 1: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 5 Minutes operation, the optical properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.

Note 2: Definition of viewing angle range and measurement system.

viewing angle is measured at the center point of the LCD.



Note 3: Definition of contrast ratio

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD is on the "White" state}}{\text{Luminance measured when LCD is on the "Black" state}}$$

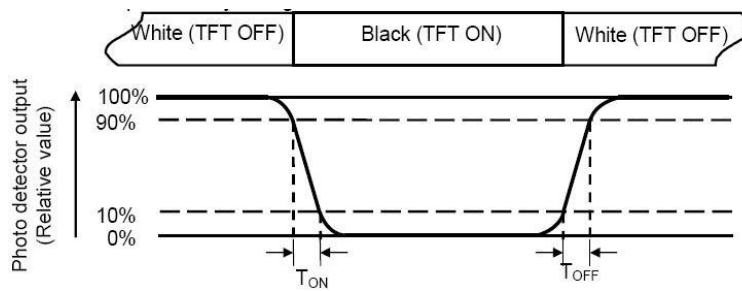
“White state”: The state is that the LCD should drive by V_{white} .

“Black state”: The state is that the LCD should drive by V_{black} .

V_{white} : TBD V V_{black} : TBD V.

Note 4: Definition of Response time

The response time is defined as the LCD optical switching time interval between “White” state and “Black” state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%.



Note 5: Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of

LCD. Note 6: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer Fig. 2). Every measuring point is placed at the center of each measuring area.

Luminance Uniformity (U) :

$$\text{UNIFORMITY:} \left[1 - \frac{\text{MAXIMUM BRIGHTNESS} - \text{MINIMUM BRIGHTNESS}}{\text{AVERAGE BRIGHTNESS}} \right] \times 100\%$$

L-----Active area length W----- Active area width

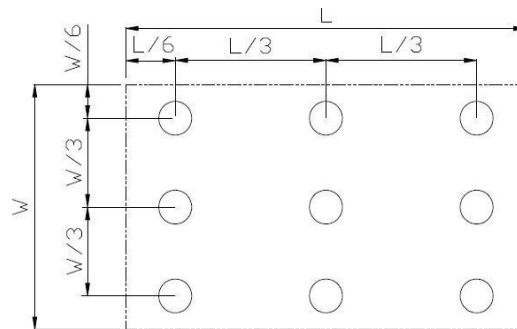
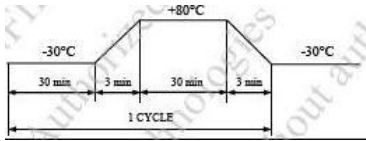


Fig. 2

Note 7: Definition of Luminance:

Measure the luminance of white state at center point.

8 .Environmental / Reliability Tests

No	Test Item	Condition	Remarks
1	High Temperature Operation	Ts=+70℃, 240hrs	Note1 IEC60068-2-1,GB2423.2
2	Low Temperature Operation	Ta=-20℃, 240hrs	IEC60068-2-1 GB2423.1
3	High Temperature Storage	Ta=+80℃, 240hrs	IEC60068-2-1 GB2423.2
4	Low Temperature Storage	Ta=-30℃, 240hrs	IEC60068-2-1 GB2423.1
5	High Temperature & High Humidity Storage	Ta=+60℃, 90% RH 240 hours	Note2 IEC60068-2-78 GB/T2423.3
6	Thermal Shock (Non-operation)	-30℃ 30 min~+80℃ 30 min,, 10 Cycles 	Start with cold temperature, End with high temperature, IEC60068-2-14,GB2423.22
7	Electro Static Discharge (NOT OPERATED)	C=150pF, R=330Ω, 1 Air:±12KV, Contact:±8KV,	IEC61000-4-2 GB/T17626.2
8	Package Drop Test	Height:80 cm, 1 corner, 3 edges, 6 surfaces	IEC60068-2-32 GB/T2423.8

Note1: Ts is the temperature of panel's surface.

Note2: Ta is the ambient temperature of sample.

Note3: Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature.

Note 4: In the standard condition, there shall be no practical problem that may affect the display function. After the reliability test, the product only guarantees operation, but doesn't guarantee all of the cosmetic specification.

9. LCD Module Design and Handling Precautions

- Please ensure V0, VCOM is adjustable, to enable LCD module get the best contrast ratio under different temperatures, view angles and positions.
- Normally display quality should be judged under the best contrast ratio within viewable area. Unexpected display pattern may come out under abnormal contrast ratio.
- Never operate the LCD module exceed the absolute maximum ratings.
- Never apply signal to the LCD module without power supply.
- Keep signal line as short as possible to reduce external noise interference.
- IC chip (e.g. TAB or COG) is sensitive to light. Strong light might cause malfunction. Light sealing structure casing is recommended.
- Make sure there is enough space (with cushion) between case and LCD panel, to prevent external force passed on to the panel; otherwise that may cause damage to the LCD and degrade its display result.
- Avoid showing a display pattern on screen for a long time (continuous ON segment).
- LCD module reliability may be reduced by temperature shock.
- When storing and operating LCD module, avoids exposure to direct sunlight, high humidity, high or low temperature. They may damage or degrade the LCD module.
- Never leave LCD module in extreme condition (max./min storage/operate temperature) for more than 48hr.
- Recommend LCD module storage conditions is 0 C~40 C <80%RH.
- LCD module should be stored in the room without acid, alkali and harmful gas.
- Avoid dropping & violent shocking during transportation, and no excessive pressure press, moisture and sunlight.
- LCD module can be easily damaged by static electricity. Please maintain an optimum anti-static working environment to protect the LCD module. (eg. ground the soldering irons properly)
- Be sure to ground the body when handling LCD module.
- Only hold LCD module by its sides. Never hold LCD module by applying force on the heat seal or TAB.
- When soldering, control the temperature and duration avoid damaging the backlight guide or diffuser which might degrade the display result such as uneven display.
- Never let LCD module contact with corrosive liquids, which might cause damage to the backlight guide or the electric circuit of LCD module.
- Only clean LCD with a soft dry cloth, Isopropyl Alcohol or Ethyl Alcohol. Other solvents (e.g. water) may damage the LCD.
- Never add force to components of LCD module. It may cause invisible damage or degrade the module's reliability.
- When mounting LCD module, please make sure it is free from twisting, warping and bending.
- Do not add excessive force on surface of LCD, which may cause the display color change abnormally.
- LCD panel is made with glass. Any mechanical shock (e.g. dropping from high place) will damage the LCD module.

9. 液晶显示模块设计和使用须知

- 请注意 V0, VCOM 的设置, 以确保液晶显示模块在不同的使用温度下以及在不同的视角和位置观察模块显示, 均能达到最佳对比度, 请务必将应用电路上设置为对比度可调。
- 请注意液晶显示模块的显示品质判定是指在正常对比度下以及视窗 (V. A) 范围内进行的, 非正常对比度下液晶可能会出现非预期的显示不良, 应注意区分。
- 请勿在最大额定值以外使用液晶显示模块。
- 请勿在没有接通电源的条件下, 给液晶显示模块输送信号。
- 请尽可能缩短信号线的连接, 以避免对液晶显示模块的信号干扰。
- 集成电路因 IC 芯片 (如 TAB 或 COG) 对紫外线极为敏感, 强光环境下可能会引起液晶显示模块功能失效, 故应采用不透光的外壳。
- 请在液晶显示模块与外壳之间保留足够的空间 (可使用衬垫), 以缓冲外力对液晶显示模块的损坏或因受力不均而产生的显示不均等异常现象。
- 避免液晶显示屏在某一画面下长时间点亮, 否则有出现残影的风险; 请通过软件每隔一段时间改变一次画面。
- 液晶显示模块的可靠性可能因温度冲击而降低。
- 请勿在阳光直射、高湿、高温或低温下储存和使用液晶显示模块, 这将造成液晶显示模块的损坏或失效。
- 请勿在极限环境 (最大/最小存储/工作温度) 下使用或放置液晶显示模块超过 48 小时以上。
- 液晶显示模块建议存储条件为: 0 C~40 C <80%RH。
- 请勿让液晶显示模块存储于带有 酸性, 碱性, 有害气体环境之中。
- 在运输过程中, 请勿让液晶显示模块跌落与猛烈震动, 同时避免 异常挤压, 高湿度, 与阳光照射。
- 液晶显示模块极易受静电损坏, 请务必保证液晶显示模块在防静电的工作环境中使用或保存。(如: 烙铁正确接地, 等)
- 拿取液晶显示模块时需注意操作人员的接地情况。
- 请手持液晶显示模块的边沿取放模块, 防止热压纸或 TAB 部位受力。
- 焊接液晶模块时, 请注意控制烙铁的温度、焊接时间, 以免烫坏导光板或偏光片, 导致显示不均等不良现象发生。
- 请勿使用洗板水等腐蚀性液体接触液晶模块, 以免腐蚀导光板或模块电路。
- 仅可使用柔软的干布, 异丙醇或乙醇清洁液晶屏表面, 其他任何溶剂 (如: 水) 都有可能损坏液晶模块。
- 请勿挤压液晶显示模块上的元器件, 以避免产生潜在的损坏或失效而影响产品可靠性。
- 装配液晶显示模块时, 请务必注意避免液晶显示模块的扭曲或变形。
- 请勿挤压液晶显示屏表面, 这将导致显示颜色的异常。
- 液晶屏由玻璃制作而成, 任何机械碰撞 (如从高处跌落) 均有可能损坏液晶显示模块。

- Protective film is attached on LCD screen. Be careful when peeling off this protective film, since static electricity may be generated.
- Polarizer on LCD gets scratched easily. If possible, do not remove LCD protective film until the last step of installation.
- When peeling off protective film from LCD, static charge may cause abnormal display pattern. The symptom is normal, and it will turn back to normal in a short while.
- LCD panel has sharp edges, please handle with care.
- Never attempt to disassemble or rework LCD module.
- If display panel is damaged and liquid crystal substance leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes promptly wash it off using soap and water.

10. CTP Mounting Instructions

10.1 Bezel Mounting (Figure 1)

- The bezel window should be bigger than the CTP active area. It should be $\geq 0.5\text{mm}$ each side.
- Gasket should be installed between the bezel and the CTP surface.
The final gap should be about $0.5\sim 1.0\text{mm}$.
- It is recommended to provide an additional support bracket for backside support when necessary (e.g. slim type TFT module without mounding structure). They should only provide appropriate support and keep the module in place.
- The mounting structure should be strong enough to prevent external uneven force or twist act onto the module.

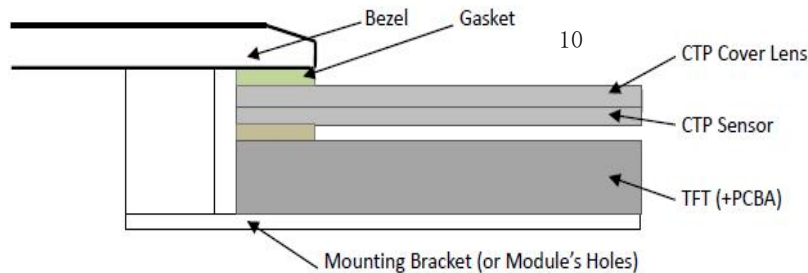
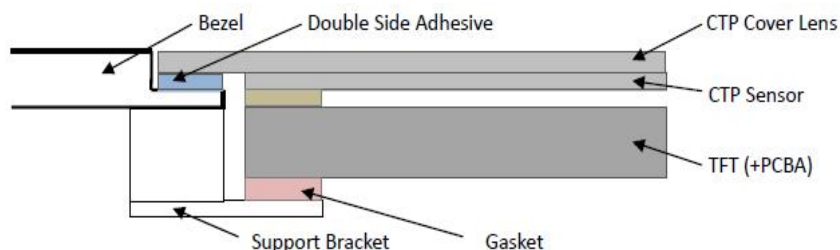


Figure 1

10.2 Surface Mounting (Figure 2)

- As the CTP assembling on the countersink area with double side adhesive.
The countersink area should be flat and clean to ensure the double side adhesive installation result.
- The Bezel is recommend to keep a gap ($\geq 0.3\text{mm}$ each side) around the cover lens for tolerance.
- It is recommended to provide an additional support bracket with gasket for backside support when necessary (e.g. TFT module without mounding structure). They should only provide appropriate support and keep the module in place.
- The mounting structure should be strong enough to prevent external uneven force or twist act onto the module



- 液晶屏表面带有保护膜，揭除保护膜时需要注意可能产生的静电。
- 因液晶显示屏表面的偏光片极易划伤，安装完成之前请尽量不要揭下保护膜。
- 请缓慢揭除保护膜，在此过程中液晶显示屏上可能会产生静电，此为正常情况，可在短时间内消失。
- 请注意避免被液晶显示屏的边缘割伤。
- 请不要试图拆卸或改造液晶显示模块。
- 当液晶显示屏出现破裂，内部液晶液体可能流出；相关液体不可吞吃，绝对不可接触嘴巴，如接触到皮肤或衣服，请使用肥皂与清水彻底清洗。

10. 电容触摸屏安装指导

10.1 面框安装（附图 1）

- 客户面框窗口应大于 CTP 动作区域，各边离动作区应 $\geq 0.5\text{mm}$ 。
- 面框与 CTP 面板间应垫有胶垫，其最终间隙约为 $0.5\sim 1.0\text{mm}$ 。
- 建议必要时在背面提供附加支架（例如无安装结构的薄型 TFT 模块），应仅利用适当支撑以保持模块位置。
- 安装结构应具有足够的强度，以防止外部不均匀力或扭曲力作用到模块上。

10.2 嵌入安装（附图 2）

- 客户面框应具有使用双面胶粘贴 CTP 的结构沉台面，其粘贴面要求平整且洁净无污染以保证粘贴牢靠。
- 考虑到制作误差，建议面框与 CTP 盖板之间四周留有 $\geq 0.3\text{mm}$ 间隙。
- 建议必要时在背面提供垫有胶垫附加支架（例如无安装结构的 TFT 模块），应仅利用适当支撑以保持模块位置。
- 安装结构应具有足够的强度，以防止外部不均匀力或扭曲力作用到模块上。

Figure 2

10.3 Additional Cover Lens Mounting (Figure 3)

- For the case of additional cover Lens mounting, it is necessary to recheck with the CTP specification about the material and thickness to ensure the functionality.
- It should keep a 0.2~0.3mm gap between the cover lens and the CTP surface..
- The cover lens window should be bigger than the active area of the CTP. It should be $\geq 0.5\text{mm}$ each side.
- It is recommended to provide an additional support bracket for backside support when necessary (e.g. slim type TFT module without mounding structure). They should only provide appropriate support and keep the module in place.
- The mounting structure should be strong enough to prevent external uneven force or twist act onto the module.

10.3 覆加盖板（附图 3）

- 需要覆加玻璃盖板的安装，为确保其功能，有必要查看产品规格书中有关盖板材料和厚度的说明。
- 玻璃盖板与 CTP 表面之间应留有 0.2~0.3mm 间隙。
- 玻璃盖板视窗应大于 CTP 动作区域，各边离动作区 $\geq 0.5\text{mm}$ 。
- 建议必要时在背面提供附加支架（例如无安装结构的薄型 TFT 模块），应仅利用适当支撑以保持模块位置。
- 安装结构应具有足够的强度，以防止外部不均匀力或扭曲力作用到模块上。

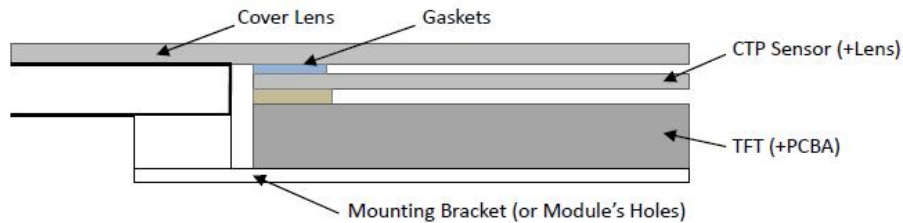


Figure 3

11. RTP Mounting Instructions

- It should bezel touching the RTP Active Area (A.A.) to prevent abnormal touch. It should left gap $D=0.2\sim 0.3\text{mm}$ in between. (Figure 4)
- Outer bezel design should take care about the area outside the A.A. Those areas contain circuit wires which is having different thickness. Touching those areas could de-form the ITO film. As a result bezel the ITO film be damaged and shorten its lifetime.
It is suggested to protect those areas with gasket (between the bezel and RTP). The suggested figures are $B\geq 0.50\text{mm}$; $C\geq 0.50\text{mm}$. (Figure 4)
- The bezel side wall should keep space $E=0.2\sim 0.3\text{mm}$ from the RTP. (Figure 4)

11. 电阻触摸屏安装指导

- 为避免面框直接压在动作区 (A. A.) 上造成误动作，面框与电阻触摸屏 (RTP) 之间应留有一定的空隙 $D=0.2\sim 0.3\text{mm}$ 之间。（附图 4）
- 设计面框时，要注意用面框保护触摸屏四周的非保证操作区域，因为布线区域在此处形成一台阶，在此区域附近操作时 ITO Film 变形较大，容易导致 ITO 损坏而降低寿命。为保护 RTP 和避免误操作，在 RTP 与面框之间垫缓冲物（Gasket），我们建议设计面框应覆盖动作区的边缘，面框边缘到 V. A. 区的距离 $B\geq 0.50\text{mm}$ ；垫圈内边缘到 V. A. 区的距离 $C\geq 0.50\text{mm}$ 。（附图 4）
- 在设计面框与 RTP 组装时，应考虑到面框内侧与 RTP 外侧的间距 $E\geq 0.2\text{mm}$ 。（附图 4）

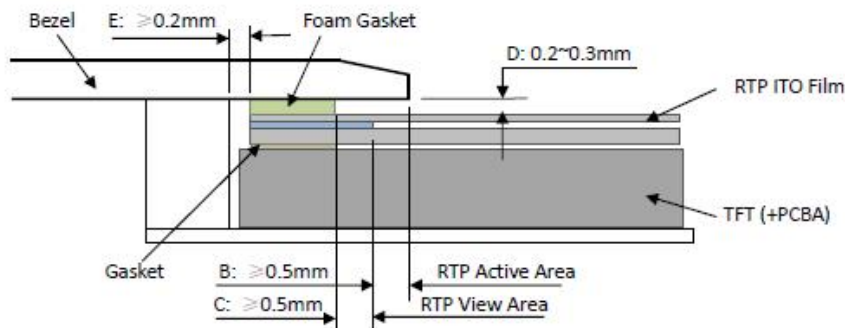


Figure 4

- In general design, RTP V.A. should be bigger than the TFT V.A. and RTP A.A. should be bigger than the TFT A.A. (Figure 5)

- 通常设计时：
RTP 的可视区 V. A. 应不小于 TFT 的可视区 V. A.
及 RTP 的动作区 A. A. 应不小于 TFT 的动作区 A. A.
(附图 5)

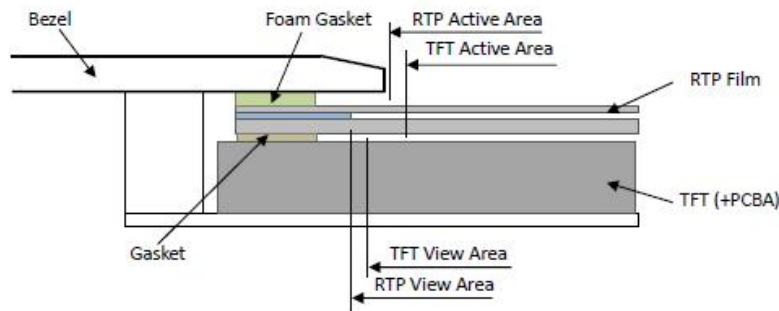
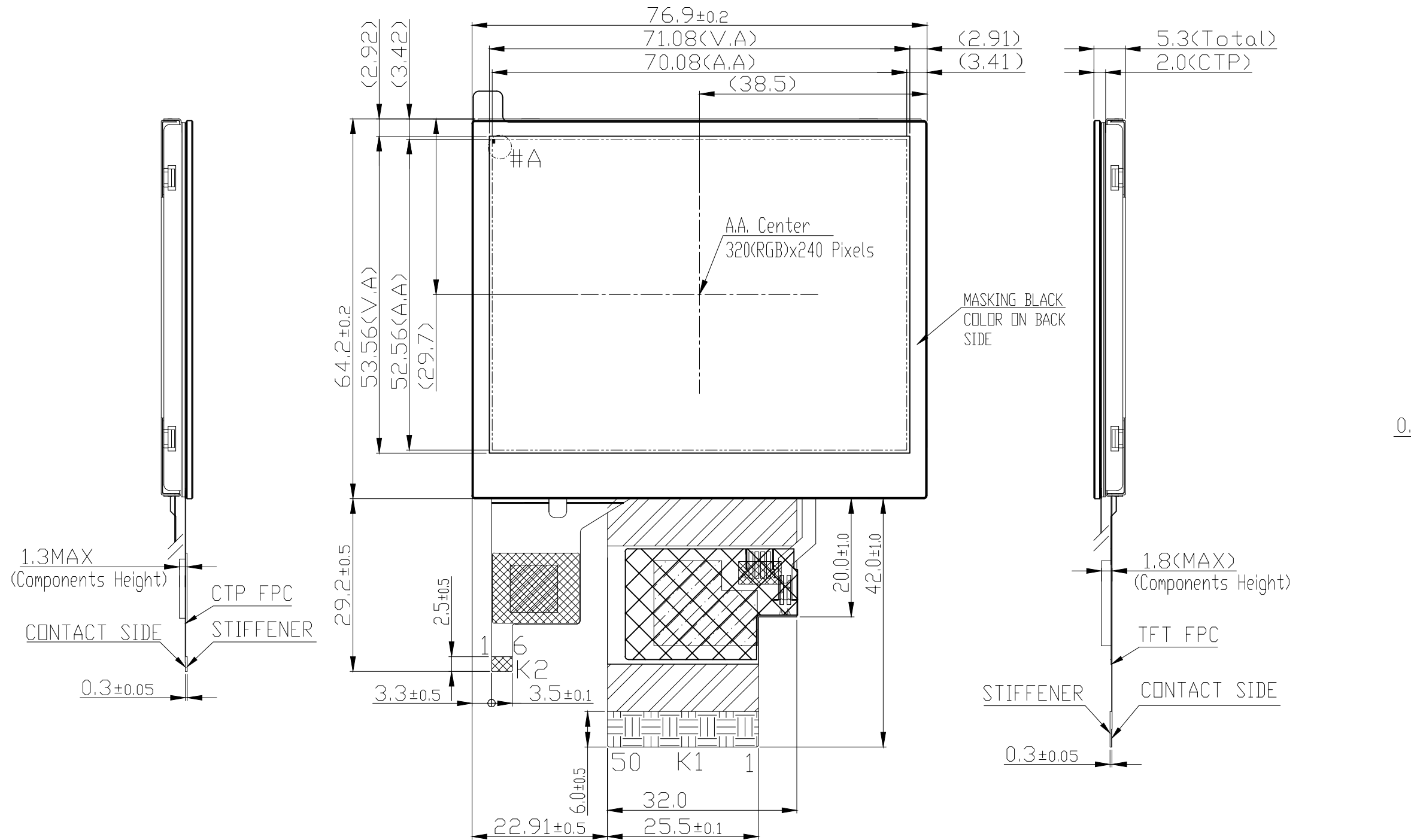


Figure 5

Warranty

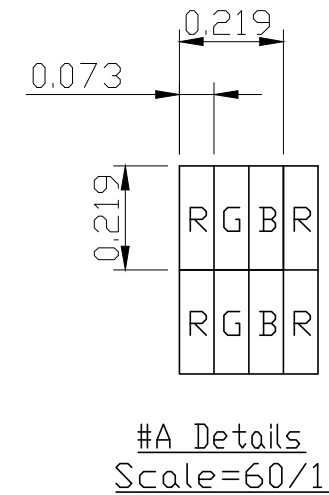
This product has been manufactured to our company's specifications as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in medical devices, nuclear power control equipment, aerospace equipment, fire and security systems, or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required. If the product is to be used in any of the above applications, we will need to enter into a separate product liability agreement.

- We cannot accept responsibility for any defect, which may arise from additional manufacturing of the product (including disassembly and reassembly), after product delivery.
- We cannot accept responsibility for any defect, which may arise after the application of strong external force to the product.
- We cannot accept responsibility for any defect, which may arise due to the application of static electricity after the product has passed our company's acceptance inspection procedures.
- When the product is in CCFL models, CCFL service life and brightness will vary according to the performance of the inverter used, leaks, etc. We cannot accept responsibility for product performance, reliability, or defect, which may arise.
- We cannot accept responsibility for intellectual property of a third part, which may arise through the application of our product to our assembly with exception to those issues relating directly to the structure or method of manufacturing of our product.



K2 Terminal	
No	Pin Name
1	VDD
2	GND
3	I2C_RST
4	TP_SCL
5	TP_SDA
6	Global_RST

K1 Terminal	
No	Pin Name
1	VBL-
2	VBL-
3	VBL+
4	VBL+
5	NC
6	/RESET
7	CSB
8	SCL
9	SDA
10	SDA
11	B7
:	:
18	B0
19	G7
:	:
26	G0
27	R7
:	:
34	R0
35	DCLK
36	HSYNC
37	VSYSN
38	ENB
39	VDD(ANALOG)
40	VDD(ANALOG)
41	NC
42	NC
43	NC
44	NC
45	NC
46	NC
47	SHUT
48	NC
49	VSS
50	VSS



- Note:
- *1. LCD Display Type : TFT, Transmissive (Full View)
 - *2. Pixel Arrangement : RGB-STRIPE
 - *3. Signal Interface : RGB_24bit
 - *4. Color Depth : 16.7M Colors
 - *5. Operating Voltage : 3.3V
 - *6. Logic Voltage : 3.3V
 - *7. Backlight : White LEDs
 - *8. Backlight Supply : 20mA (VF=18.0V, TYP)
 - *9. Touch Panel Type : Capacitive Touch Panel
 - *10. CTP Driver IC: ST1633i
 - *11. Recommended Connector :
K1: FH19SC-50S-0.5SH(HIROSE) Or Equivalent
K2: FH34SRJ-6S-0.5SH(50) Or Equivalent
 - *12. Operating Temperature : $-20^{\circ}\text{C} \sim 70^{\circ}\text{C}$
 - *13. Storage Temperature : $-30^{\circ}\text{C} \sim 80^{\circ}\text{C}$

C		
B		
A		
Rev	Note	Date
Dwg	Title	
LMT035ENAFWA-NND Outline Dwg		
Dwg No.	MK-007018-1-1	Date
Scale	6/5	2020-08-24
Tol.	± 0.3	
Unit	mm	
Paper Size	A3	
Approved	Checked	Drawn
		Luo Lin

TOPWAY