

SEMITRANS® 3

SiC MOSFET Module

SKM350MB120SCH17

Features*

- Full Silicon Carbide (SiC) power module
- High reliability 2nd Generation SiC MOSFETs
- Optimized for fast switching and lowest power losses
- High humidity robustness (HV-H3TRB proof)
- External SiC Schottky Barrier Diode embedded
- Insulated copper baseplate using DBC technology (Direct Bonded Copper)
- Improved thermal performances with Aluminium Nitride (AIN) substrate
- UL recognized, file no. E63532

Typical Applications

- High frequency power supplies
- AC inverters
- Traction APU
- EV Chargers
- Industrial Test Systems

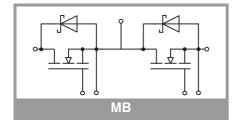
Remarks

- Case temperature limited to T_C = 125°C max.
- Recommended T_{jop} = -40 ... +150°C
- Gate-Source SURGE VOLTAGE (t_{surge}<300ns), V_{GS_surge} = -10V ... +26V

Absolute Maximum Ratings							
Symbol	Conditions		Values	Unit			
MOSFET							
V_{DSS}			1200	V			
I _D	T _i = 175 °C	T _c = 25 °C	478	Α			
	1, = 173 0	T _c = 80 °C	380	Α			
I _{DM}			1280	Α			
I _{DRM}			904	Α			
V_{GS}			-6 22	V			
Tj			-40 175	°C			
Integrated body diode							
I _{FM}			1280	Α			
I _{FRM}			904	Α			

Absolute Maximum Ratings							
Symbol	Conditions		Values	Unit			
Inverse diode							
V_{RRM}	T _j = 25 °C		1200	V			
l _F	T _j = 175 °C	T _c = 25 °C T _c = 80 °C	187	Α			
		T _c = 80 °C	143	Α			
I _{Fnom}			100	Α			
I _{FRM}			300	Α			
I _{FSM}	$t_p = 8.3 \text{ ms, sin } 180^{\circ}, T_j = 25 ^{\circ}\text{C}$		373	Α			
Tj			-40 175	°C			

Absolute Maximum Ratings						
Symbol	Ol Conditions Values					
Module						
I _{t(RMS)}		500	Α			
T _{stg}	module without TIM	-40 125	°C			
V _{isol}	AC sinus 50 Hz, t = 1 min	4000	V			





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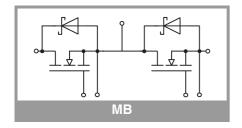
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Characteristics								
Symbol	Conditions	min.	typ.	max.	Unit			
MOSFET								
$V_{(BR)DSS}$	$V_{GS} = 0 \text{ V}, I_{D} = 1 \text{ m}$	A, T _j = 25 °C	1200			V		
$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 71.2$	2 mA	1.6		4	V		
I _{DSS}	$V_{GS} = 0 \text{ V}, V_{DS} = 12$	200 V, T _j = 25 °C			2	mA		
I _{GSS}	$V_{GS} = 22 \text{ V}, V_{DS} = 0$) V			800	nA		
R _{DS(on)}	$V_{GS} = 18 \text{ V}$	T _j = 25 °C		5.6	7.0	mΩ		
	I _D = 176 A chiplevel	T _j = 150 °C		9.5		mΩ		
C _{iss}	V _{GS} = 0 V	T _j = 25 °C		34.5		nF		
Coss	$V_{DS} = 800 \text{ V}$	T _j = 25 °C		1.10		nF		
C _{rss}	f = 1 MHz	T _j = 25 °C		0.15		nF		
R _{Gint}	T _j = 25 °C			0.6		Ω		
Q_{G}	V _{DD} =600V, V _{GS} =-5		1850		nC			
t _{d(on)}	$I_D = 175 \text{ A}$	T _j = 150 °C		64		ns		
t _r		T _j = 150 °C		10		ns		
t _{d(off)}		T _j = 150 °C		183		ns		
t _f	$R_{Goff} = 0.5 \Omega$	T _j = 150 °C		33		ns		
E _{on}	$di/dt_{on} = 13.5 \text{ kA/}$	T _j = 150 °C		1.69		mJ		
E _{off}	μ s di/dt _{off} = 7.1 kA/ μ s dv/dt _{on} = 10.5 kV/ μ s dv/dt _{off} = 28 kV/ μ s	T _j = 150 °C		1.31		mJ		
R _{th(j-c)}	per MOSFET				0.055	K/W		
R _{th(c-s)}	per MOSFET (λ _{grea}		0.03		K/W			

Characteristics							
Symbol	Conditions	min.	typ.	max.	Unit		
Inverse d	iode						
$V_F = V_{EC}$	I _F = 100 A	T _j = 25 °C		1.40	1.60	V	
	chiplevel	T _j = 150 °C		1.80	2.10	V	
V_{F0}	chiplevel	T _j = 25 °C		0.95	1.05	V	
	Chipievei	T _j = 150 °C		0.80	0.90	V	
r _F	chiplevel	T _j = 25 °C		4.5	5.5	mΩ	
	Chipievei	T _j = 150 °C		10.0	12	mΩ	
C _j	parallel to C_{oss} , $f = 1$ MHz, $V_R = 800$ V, $T_i = 25$ °C			0.42		nF	
Q _c	$V_R = 800 \text{ V, di/dt}_{off} = 500 \text{ A/}\mu\text{s,}$ $T_j = 25 ^{\circ}\text{C}$			0.33		μC	
R _{th(j-c)}	per diode				0.24	K/W	
R _{th(c-s)}	per diode (λ _{grease} =0.81 W/(m*K))			0.076		K/W	





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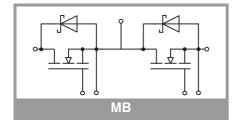
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Characte	eristics					
Symbol	Conditions	min.	typ.	max.	Unit	
Module	•					•
L _{DS}				15		nΗ
R _{DD'+SS'}	measured per switch	T _C = 25 °C		0.55		mΩ
		T _C = 125 °C		0.85		mΩ
R _{th(c-s)1}	calculated without thermal coupling (λ _{grease} =0.81 W/(m*K))			0.011		K/W
R _{th(c-s)2}	including thermal coupling, T _s underneath module (\(\lambda_{\text{qrease}} = 0.81 \) W/(m*K))			0.015		K/W
Ms	to heat sink M6				5	Nm
Mt		to terminals M6	2.5		5	Nm
	1					Nm
w					325	g



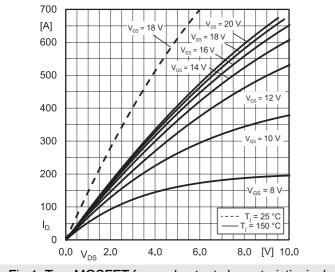


Fig.1: Typ. MOSFET forward output characteristic, incl. $R_{DD'+SS'}$

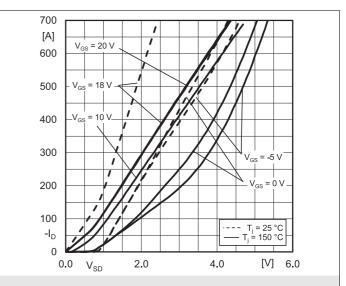


Fig. 2: Typ. reverse output characteristic, incl. R_{DD'+ SS'}

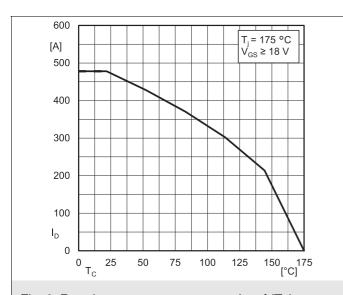


Fig. 3: Rated current vs. temperature $I_D = f(T_C)$

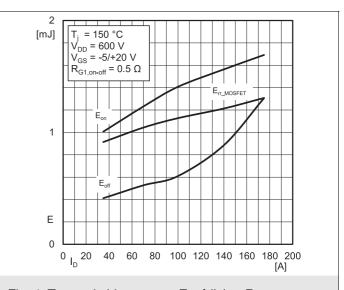


Fig. 4: Typ. switching energy $E = f(I_D)$ at R_{G1}

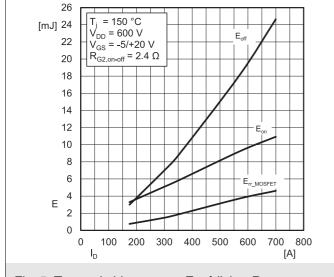


Fig. 5: Typ. switching energy $E = f(I_D)$ at R_{G2}

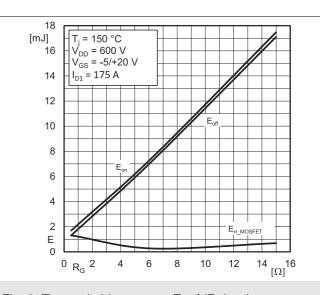


Fig. 6: Typ. switching energy $E = f(R_G)$ at I_{D1}

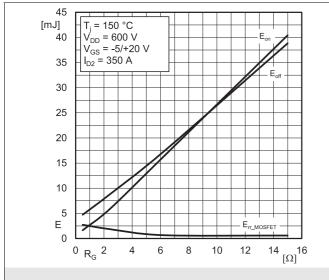


Fig. 7: Typ. switching energy $E = f(R_G)$ at I_{D2}

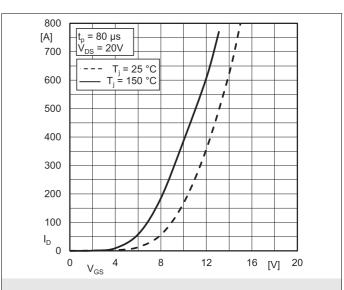


Fig. 8: Typ. MOSFET transfer characteristic

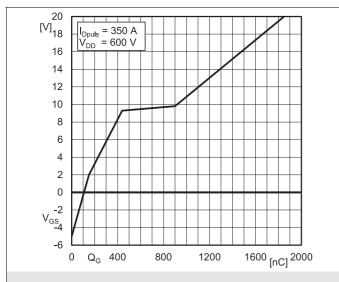


Fig. 9: Typ. gate charge characteristic

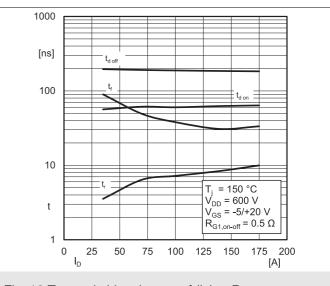


Fig. 10:Typ. switching times $t = f(I_D)$ at R_{G1}

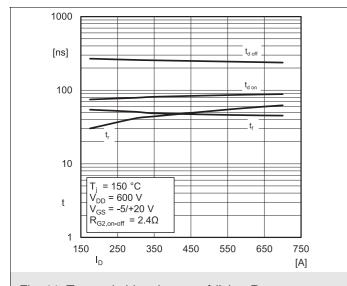


Fig. 11: Typ. switching times $t = f(I_D)$ at R_{G2}

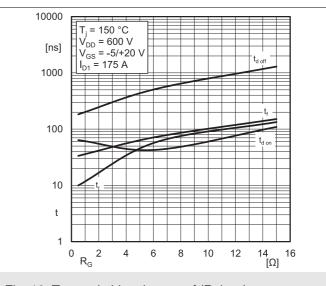


Fig. 12: Typ. switching times $t = f(R_G)$ at I_{D1}

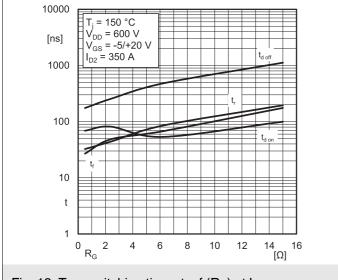


Fig. 13: Typ. switching times $t = f(R_G)$ at I_{D2}

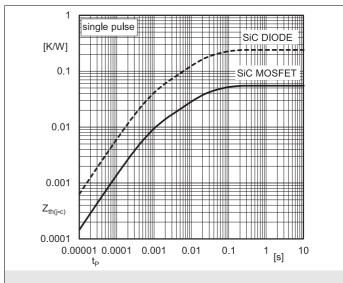


Fig. 14: Transient thermal impedance

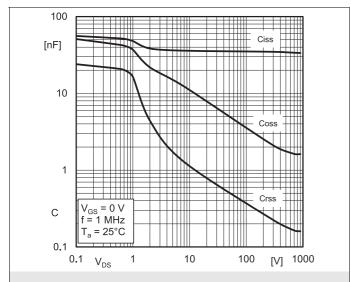
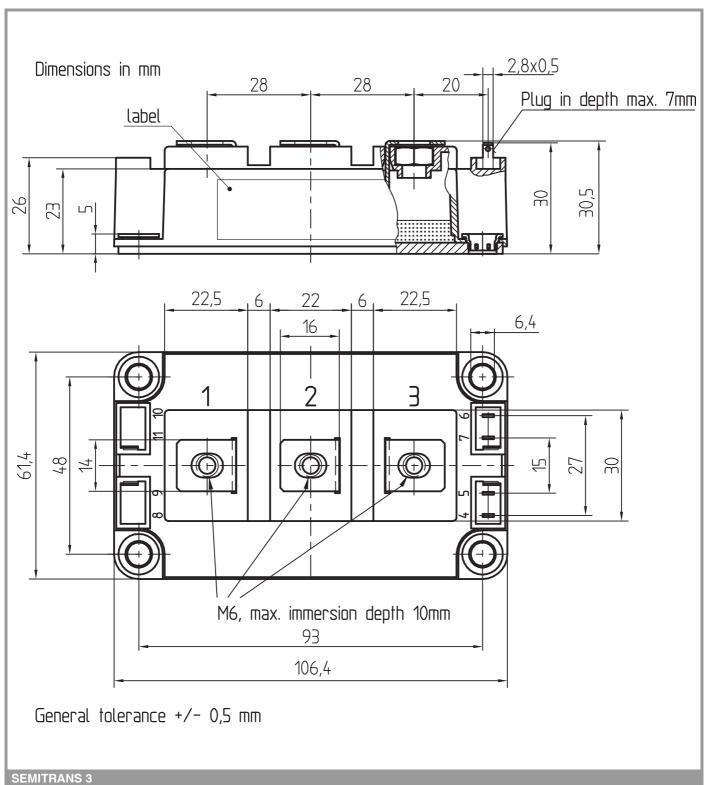
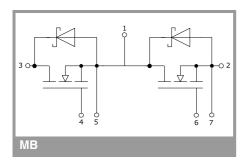


Fig. 19: Capacitances vs. drain-source voltage





This is an electrostatic discharge sensitive device (ESDS) due to international standard IEC 61340.

*IMPORTANT INFORMATION AND WARNINGS

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